

Electronic Information Disclosure Statement

LOW POWER LOW VOLTAGE TRANSISTOR-TRANSISTOR LOGIC I/O DRIVER

1c930 U.S. PTO
10/064708
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Application:
Confirmation:
Applicant(s): Gary Carpenter
Docket Number: BUR920020011
Group Art Unit:
Examiner:
search string: (6271679 or 6097113 or 5977795).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Citation No.	Patent Number	Date	Bar Code	Patentee	Class	Subclass
h	P01	6271679	2001-08-07		McClintock et al.	326	38
h	P02	6097113	2000-08-01		Teraoka et al.	307	125
h	P03	5977795	1999-11-02		Lee	326	70

Remarks

(Remarks are not for responding to an office action.)

Abstract of JP913909A1; publication date 1998-05-12; Yoo Jei-Hwan (KR). Abstract of JP11203866A; publication date 2000-02-29; Kobashi Hisao (JP), Sakurai Miki (JP), Tanida Susumu (JP).

Signature

Examiner Name	Date
<i>James n Cho</i>	<i>2-5-04</i>